

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**APPLICANT(S):** Hsu et al.

**SERIAL NO.:** Not Yet Assigned

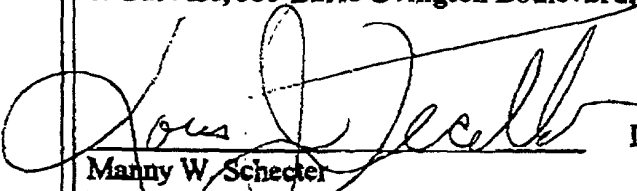
**FILED:** Concurrently Herewith

**FOR:** **DRAM ARRAY INTERCHANGEABLE BETWEEN  
SINGLE-CELL AND TWIN-CELL ARRAY OPERATION**

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